

ABSTRACT OF THE DISCLOSURE

A chip type power inductor comprising: a stack body where a magnetic substance which forms a magnetic core stacked with a plurality of layers and non-magnetic layers inserted to inside of the magnetic substance which forms a magnetic core are formed as one unit; coil patterns formed on either upper surfaces or lower surfaces of the plurality of layers of the magnetic substance which forms a magnetic core; and via holes formed at the plurality of layers constituting the magnetic substance which forms a magnetic core in order to electrically connect the coil patterns. A magnetic saturation is restrained by a non-magnetic layer formed in the power inductor, so that a DC bias characteristic corresponding to several hundreds of mA~1A which could not be realized by the conventional multi-layer chip power inductor can be obtained